Vikram Jandhyala is a professor of electrical engineering and the director of the applied computational engineering lab at the University of Washington (UW). He received his BTech EE from IIT Delhi in 1993, and his MS and PhD in EE from the University of Illinois at Urbana-Champaign (UIUC) in 1995 and 1998, respectively. He developed boundary element solvers at Ansoft Corporation, Pittsburgh, from 1998-2000 prior to joining UW in 2000. He is the chair of the UW EE professional masters program, and is an inaugural Entrepreneurial Faculty Fellow in UW's Center for Commercialization. He is founder and chairman of Physware, Inc., a venture-funded startup in the area of scalable electronic design automation. His research interests include several aspects of large-scale simulation, physics-based computing algorithms, and electronic design automation. He has published approximately 200 papers in peer-reviewed journals and conferences and has directed research funded by DARPA, NSF, NASA, SRC, INTEL, IBM, the SBIR program, AFRL, Navy, and other sponsors. Prof. Jandhyala is a recipient of the NSF CAREER award, Chair's award and Outstanding Research Advisor award at UWEE, a NASA inventor award, and graduate research awards from IEEE and UIUC.